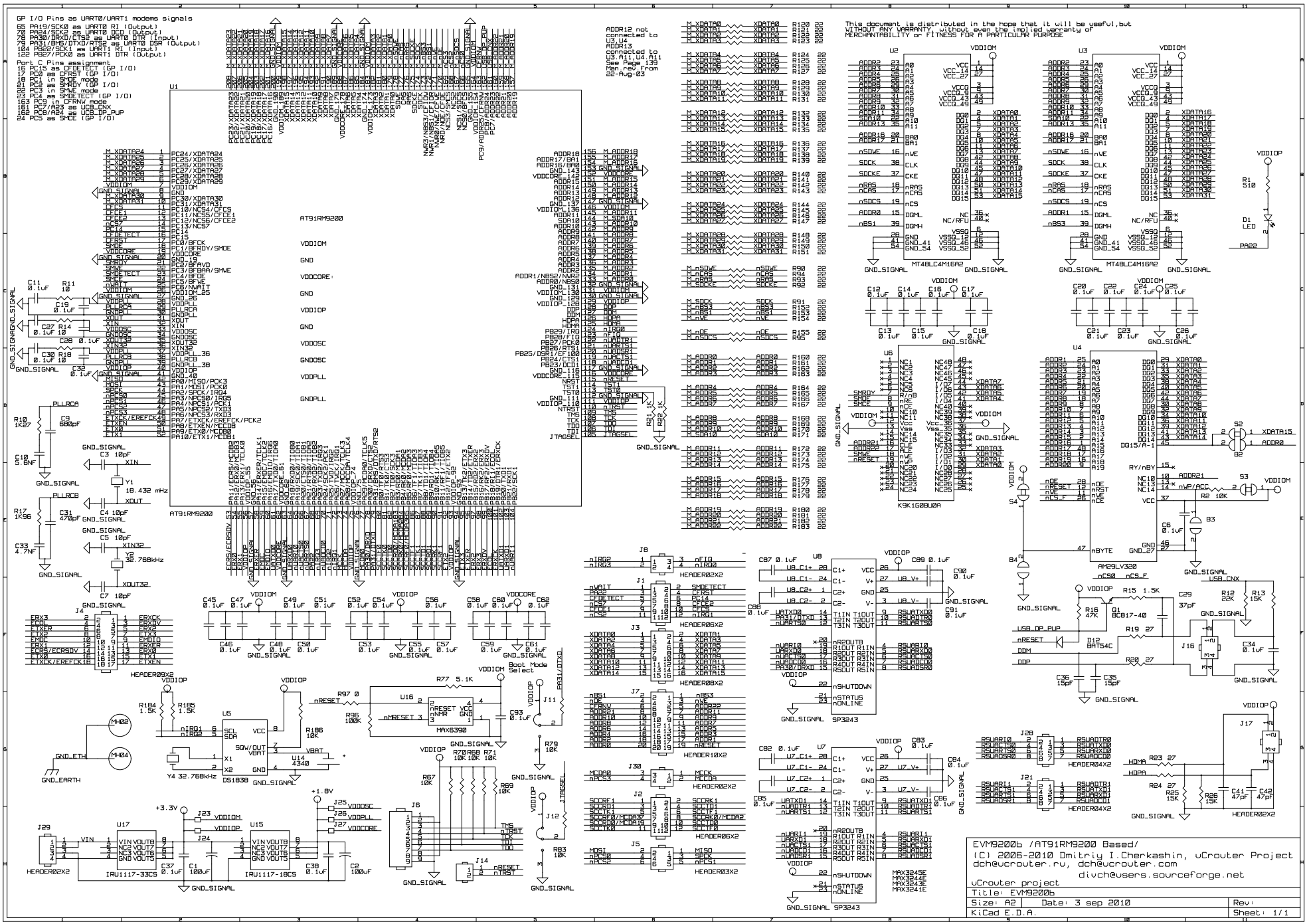
GP I/O Pins as UART0/UART1 modems signals
65 PA19/SCK0 as UART0 RI (Output)
70 PA24/SCK2 as UART0 DCD (Output)
78 PA30/DRXD/CTS2 as UART0 DTR (Input)
79 PA31/BMS/DTXD/RTS2 as UART0 DSR (Output)
104 PB22/SCK1 as UART1 RI (Input)
122 PB27/PCK0 as UART1 DTR (Output)
Port C Pins assignment
16 PC15 as CFDETECT (GP I/O)
17 PC0 as CFRST (GP I/O)
18 PC1 in SMOE mode
21 PC2 as SMRDY (GP I/O)
22 PC3 in SMWE mode
23 PC4 as SMDETECT (GP I/O)
163 PC9 in CFRNW mode
161 PC7/A23 as UCB_CNX
162 PC8/A24 as UCB_DP_PUP
24 PC5 as SMCE (GP I/O)
ADDR12 not connected to U3,U4
ADDR13 connected to U3.A11,U4.A11
See Page 139 Man.rev.from 22-Aug-03
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AT91RM9200
MT48LC4M16A2
K9K1G08U0A
AM29LV320
SP3243
MAX6390
DS1838
IRU1117-33CS
IRU1117-18CS
HEADER02X2
HEADER06X2
HEADER08X2
HEADER10X2
HEADER03X2
HEADER04X2
Boot Mode Select
EVM9200b /AT91RM9200 Based/
(C) 2006-2010 Dmitriy I.Cherkashin, vCrouter Project
dch@vcrouter.ru, dch@vcrouter.com
divch@users.sourceforge.net
vCrouter project
Title: EVM9200b
Size: A2
Date: 3 sep 2010
Rev:
KiCad E.D.A.
Sheet: 1/1